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Announcement of a Dissertation Thesis Defence

Doctoral Student: **Ing. Jiří Kvasnička**

Dissertation Thesis Title: Reliability Analysis of SRAM-based Field-Programmable Gate Arrays

Doctoral Study Program: P1801 Informatics

Supervisor: doc. Ing. Hana Kubátová, CSc., FIT

Committee Composition: prof. Ing. Pavel Tvrđík, CSc., FIT ČVUT (chairperson)
doc. Ing. Petr Fišer, Ph.D., FIT ČVUT (vice-chairperson)
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doc. Michel Renovell, Ph.D., LIRMM, FR (reviewer)
prof. Ing. Viera Stopjaková, Ph.D., STU, SR (reviewer)
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doc. Ing. Josef Dobeš, CSc., FEL ČVUT

Date and time: **January 21st, 2014 at 11:00 a.m.**

Place: room Nr. A-1452, 14th floor, Faculty of Information Technology,
Thákurova 7, 160 00 Praha 6

The dissertation thesis is available for public persual at the Office of Science and Research of FIT CTU in Prague, Thakurova 9, 160 00 Prague 6, 3rd floor, room Nr. 308.

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