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## Announcement of a Dissertation Thesis Defence

Doctoral Student: **Ing. Jaroslav Borecký**

Dissertation Thesis Title: Dependable Systems Design Methods for FPGAs

Doctoral Study Program: P1801 Informatics

Supervisor: doc. Ing. Hana Kubátová, CSc., FIT CTU in Prague

Committee Composition: prof. Ing. Pavel Tvrđík, CSc., FIT CTU in Prague (chairperson)  
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Prof. Smail Niar, Ph.D., University of Valenciennes, France (reviewer)  
Prof. Dr. Andreas Steininger, Vienna University of Technology, Austria (reviewer)  
doc. Ing. Vlastimil Vavřička, CSc., UWB, Plzen (reviewer)

Date and time: **January 21<sup>st</sup>, 2016 at 14:30**

Venue: room Nr. A-1435, 14<sup>th</sup> floor, Faculty of Information Technology,  
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The dissertation thesis is available for public persual at the Office of Science and Research of FIT CTU in Prague, Thakurova 9, 160 00 Prague 6, 3<sup>rd</sup> floor, room Nr. 308.

Prague, December 4<sup>th</sup>, 2015

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