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Announcement of a Dissertation Thesis Defence

Doctoral Student: **Ing. Jiří Balcárek**

Dissertation Thesis Title: Implicit Representations in Testing and Dependability of Digital Circuits

Doctoral Study Program: P1801 Informatics

Supervisor: doc. Ing. Jan Schmidt, Ph.D., FIT CTU in Prague

Supervisor-specialist: doc. Ing. Petr Fišer, Ph.D., FIT CTU in Prague

Committee Composition: doc. Ing. Hana Kubátová, CSc., FIT CTU in Prague (chairperson)
prof. Ing. Pavel Tvrdlík, CSc., FIT CTU in Prague (vice- chairperson)
prof. Ing. Róbert Lórencz, CSc., FIT CTU in Prague
doc. Ing. Josef Dobeš, CSc., FEE CTU in Prague
doc. Ing. Richard Růžička, Ph.D., FIT VUT, Brno
prof. Ing. Ondřej Novák, CSc., FM TUL
doc. Ing. Stanislav Racek, CSc., UWB Plzen
prof. Ing. Tomáš Hruška, CSc., FIT VUT, Brno (reviewer)
Prof. Raimund Ubar, PhD., TTU Tallinn, Estonia (reviewer)
Prof. Matteo Sonza Reorda, PhD., Politecnico di Torino, Italy (reviewer)

Date and time: **February 23th, 2017 at 11:00**

Venue: room Nr. A-1452, 14th floor, Faculty of Information Technology,
Thákurova 7, 160 00 Praha 6

The dissertation thesis is available for public persual at the Office of Science and Research of FIT CTU in Prague, Thakurova 9, 160 00 Prague 6, 3rd floor, room Nr. 308.

Prague, January 6th, 2017

doc. RNDr. Ing. Marcel Jiřina, Ph.D.
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